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<u>L12</u>	(front end\$) near7 (two near5 input\$)	61	<u>L12</u>
<u>L11</u>	L10 and l1	21	<u>L11</u>
<u>L10</u>	717/136,137,139,141,142,143,144,146,150.ccls.	1077	<u>L10</u>
<i>DB=TDBD; PLUR=YES; OP=ADJ</i>			
<u>L9</u>	(front end) near5 (specification\$ or source\$ or program\$ or original\$) and (front end) near7 (grammar\$ or syntax\$) and (back end\$)and (tree\$ or node\$)	0	<u>L9</u>
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	(front end) near7 (grammar\$ or syntax\$) and (back end\$)and (tree\$ or node\$)		
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	<i>DB=EPAB; PLUR=YES; OP=ADJ</i>		
<u>L6</u>	(front end) near5 (specification\$ or source\$ or program\$ or original\$) and (front end) near7 (grammar\$ or syntax\$) and (back end\$)and (tree\$ or node\$)	0	<u>L6</u>
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<u>L5</u>	(front end) near5 (specification\$ or source\$ or program\$ or original\$) and (front end) near7 (grammar\$ or syntax\$) and (back end\$)and (tree\$ or node\$)	0	<u>L5</u>
	<i>DB=PGPB; PLUR=YES; OP=ADJ</i>		
<u>L4</u>	(front end) near5 (specification\$ or source\$ or program\$ or original\$) and (front end) near7 (grammar\$ or syntax\$) and (back end\$)and (tree\$ or node\$)	4	<u>L4</u>
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<u>L3</u>	L2 and (tree\$ or node\$)	30	<u>L3</u>
<u>L2</u>	L1 and (back end\$)	37	<u>L2</u>
<u>L1</u>	(front end) near5 (specification\$ or source\$ or program\$ or original\$) and (front end) near7 (grammar\$ or syntax\$ or syntac\$)	54	<u>L1</u>

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IEEE JNL IEEE Journal or Magazine

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IEEE CNF IEEE Conference Proceeding

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☐ 1. Visualization/animation of programs based on abstract representations a mappingsPereira, M.J.V.; Henriques, P.R.;  
Human-Centric Computing Languages and Environments, 2001. Proceedings  
on

5-7 Sept. 2001 Page(s):373 - 381

Digital Object Identifier 10.1109/HCC.2001.995294

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IEEE JNL IEEE Journal or Magazine

IEEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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**1. A VHDL based design environment for VLSI circuits**

Vyas, M.C.; Reddy, G.N.;  
Southeastcon '89. Proceedings. 'Energy and Information Technologies in the S  
9-12 April 1989 Page(s):401 - 405 vol.2  
Digital Object Identifier 10.1109/SECON.1989.132409

[AbstractPlus](#) | Full Text: [PDF](#)(312 KB) IEEE CNF**2. DML-a meta-language and system for the generation of practical and efficient from denotational specifications**

Patterson, M.; Fritzson, P.;  
Computer Languages, 1992., Proceedings of the 1992 International Conference  
20-23 April 1992 Page(s):127 - 136  
Digital Object Identifier 10.1109/ICCL.1992.185475

[AbstractPlus](#) | Full Text: [PDF](#)(760 KB) IEEE CNF**3. Visualization/animation of programs based on abstract representations and mappings**

Pereira, M.J.V.; Henriques, P.R.;  
Human-Centric Computing Languages and Environments, 2001. Proceedings  
on  
5-7 Sept. 2001 Page(s):373 - 381  
Digital Object Identifier 10.1109/HCC.2001.995294

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## 21 [Retargetable Compiler Code Generation](#)



Mahadevan Ganapathi, Charles N. Fischer, John L. Hennessy

December 1982 **ACM Computing Surveys (CSUR)**, Volume 14 Issue 4

Publisher: ACM Press

Full text available: [pdf\(1.93 MB\)](#)Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

## 22 [Toward an engineering discipline for grammarware](#)



Paul Klint, Ralf Lämmel, Chris Verhoef

July 2005 **ACM Transactions on Software Engineering and Methodology (TOSEM)**,

Volume 14 Issue 3

Publisher: ACM Press

Full text available: [pdf\(710.42 KB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Grammarware comprises grammars and all grammar-dependent software. The term *grammar* is meant here in the sense of all established grammar formalisms and grammar notations including context-free grammars, class dictionaries, and XML schemas as well as some forms of tree and graph grammars. The term *grammar-dependent software* refers to all software that involves grammar knowledge in an essential manner.

Archetypal examples of grammar-dependent software are parsers, program converters ...

**Keywords:** Grammarware, automated software engineering, best practices, generic language technology, grammar-dependent software, grammars, language processing, metamodeling, model-driven development, parsers, software evolution, software transformation

## 23 [A Tutorial on Algol 68](#)



Andrew S. Tanenbaum

June 1976 **ACM Computing Surveys (CSUR)**, Volume 8 Issue 2

Publisher: ACM Press

Full text available: [pdf\(2.92 MB\)](#)Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

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# 1 [GENOA—a customizable, front-end-retargetable source code analysis framework](#)



Premkumar T. Devanbu

April 1999 **ACM Transactions on Software Engineering and Methodology (TOSEM)**,

Volume 8 Issue 2

Publisher: ACM Press

Full text available: pdf(241.27 KB)

 Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

Code analysis tools provide support for such software engineering tasks as program understanding, software metrics, testing, and reengineering. In this article we describe GENOA, the framework underlying application generators such as Aria and GEN++ which have been used to generate a wide range of practical code analysis tools. This experience illustrates front-end retargetability of GENOA; we describe the features of the GENOA framework that allow it to be ...

**Keywords:** code inspection, metrics, reverse engineering, source analysis

# 2 [A single-pass syntax-directed front end for Ada](#)



T. P. Baker

June 1982 **ACM SIGPLAN Notices, Proceedings of the 1982 SIGPLAN symposium on Compiler construction SIGPLAN '82**, Volume 17 Issue 6

Publisher: ACM Press

Full text available: pdf(842.95 KB)

 Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This paper describes the front-end processor of an Ada compiler that is under development at Florida State University. The compiler is coded in Pascal, to execute on a CDC Cyber system, and is presently targeted to the Z8000 microprocessor architecture. Owing at least in part to the peculiar origins and changing goals of this project, the front end processor is rather unlike those of the other Ada compilers of which we know. Perhaps its most distinctive feature is that it operates in one pa ...

# 3 [Handling context-sensitive syntactic issues in the design of a front-end for a MATLAB compiler](#)


 Pramod G. Joisha, Abhay Kanhere, Prithviraj Banerjee, U. Nagaraj Shenoy, Alok Choudhary  
 March 2001 **ACM SIGAPL APL Quote Quad**, Volume 31 Issue 3

Publisher: ACM Press


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IEEE CNF IEEE Conference Proceeding

IEEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

- ☐ 1. **Phobos: a front-end approach to extensible compilers**  
 Granicz, A.; Hickey, J.;  
 System Sciences, 2003. Proceedings of the 36th Annual Hawaii International C  
 6-9 Jan 2003 Page(s):10 pp.  
 Digital Object Identifier 10.1109/HICSS.2003.1174890  
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- ☐ 1. **Checking Inside the Black Box: Regression Testing by Comparing Value**  
Tao Xie; Notkin, D.;  
Software Engineering, IEEE Transactions on  
Volume 31, Issue 10, Oct. 2005 Page(s):869 - 883  
Digital Object Identifier 10.1109/TSE.2005.107  
[AbstractPlus](#) | Full Text: [PDF](#)(984 KB) IEEE JNL.
- ☐ 2. **Generating efficient protocol code from an abstract specification**  
Castelluccia, C.; Dabbous, W.; O'Malley, S.;  
Networking, IEEE/ACM Transactions on  
Volume 5, Issue 4, Aug. 1997 Page(s):514 - 524  
Digital Object Identifier 10.1109/90.649465  
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(164 KB) IEEE JNL.
- ☐ 3. **Generator of efficient strongly typed abstract syntax trees in Java**  
van den Brand, M.; Moreau, P.-E.; Vinju, J.;  
Software, IEE Proceedings- [see also Software Engineering, IEE Proceedings]  
Volume 152, Issue 2, 8 April 2005 Page(s):70 - 78  
Digital Object Identifier 10.1049/ip-sen:20041181  
[AbstractPlus](#) | Full Text: [PDF](#)(570 KB) IEE JNL.
- ☐ 4. **The Performance Penalty of XML for Program Intermediate Representatio**  
Anderson, P.;  
Source Code Analysis and Manipulation, 2005. Fifth IEEE International Works  
30-01 Sept. 2005 Page(s):193 - 202  
Digital Object Identifier 10.1109/SCAM.2005.25  
[AbstractPlus](#) | Full Text: [PDF](#)(288 KB) IEEE CNF
- ☐ 5. **A system for specification and rapid prototyping of application command**  
Stelovsky, J.; Sugaya, H.;  
Software Engineering, IEEE Transactions on  
Volume 14, Issue 7, July 1988 Page(s):1023 - 1032  
Digital Object Identifier 10.1109/32.42742  
[AbstractPlus](#) | Full Text: [PDF](#)(884 KB) IEEE JNL.
- ☐ 6. **The halo algorithm-an algorithm for hierarchical design of rule checking**  
Hedenstierna, N.; Jeppson, K.O.;



Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction:  
Volume 12, Issue 2, Feb. 1993 Page(s):265 - 272  
Digital Object Identifier 10.1109/43.205006

[AbstractPlus](#) | Full Text: [PDF](#)(792 KB) IEEE JNL

- ☐ **7. Automated scalability analysis of message-passing parallel programs**  
Sarukkai, S.R.; Mehra, P.; Block, R.J.;  
Parallel & Distributed Technology: Systems & Applications, IEEE [see also IEE  
Volume 3, Issue 4, Winter 1995 Page(s):21 - 32  
Digital Object Identifier 10.1109/88.473611  
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(1096 KB) IEEE JNL
- ☐ **8. A\*: a language for implementing language processors**  
Ladd, D.A.; Ramming, J.C.;  
Software Engineering, IEEE Transactions on  
Volume 21, Issue 11, Nov. 1995 Page(s):894 - 901  
Digital Object Identifier 10.1109/32.473218  
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(736 KB) IEEE JNL
- ☐ **9. Collection-aware optimum sequencing of operations and closed-form sol  
distribution of a divisible load on arbitrary processor trees**  
Barlas, G.D.;  
Parallel and Distributed Systems, IEEE Transactions on  
Volume 9, Issue 5, May 1998 Page(s):429 - 441  
Digital Object Identifier 10.1109/71.679214  
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(440 KB) IEEE JNL
- ☐ **10. Clock skew reduction in ASIC logic design: a methodology for clock tree**  
Balboni, A.; Costi, C.; Pellencin, M.; Quadrini, A.; Sciuto, D.;  
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction  
Volume 17, Issue 4, April 1998 Page(s):344 - 356  
Digital Object Identifier 10.1109/43.703824  
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(340 KB) IEEE JNL
- ☐ **11. Efficient parallel execution of irregular recursive programs**  
Prechelt, L.; Hanssger, S.U.;  
Parallel and Distributed Systems, IEEE Transactions on  
Volume 13, Issue 2, Feb. 2002 Page(s):167 - 178  
Digital Object Identifier 10.1109/71.983944  
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(281 KB) IEEE JNL
- ☐ **12. Architecture and synthesis for on-chip multicycle communication**  
Cong, J.; Yiping Fan; Guoling Han; Xun Yang; Zhiru Zhang;  
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction  
Volume 23, Issue 4, April 2004 Page(s):550 - 564  
Digital Object Identifier 10.1109/TCAD.2004.825872  
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(992 KB) IEEE JNL
- ☐ **13. Faster Isosurface Ray Tracing Using Implicit KD-Trees**  
Wald, I.; Friedrich, H.; Marmitt, G.; Slusallek, P.; Seidel, H.-P.;  
Visualization and Computer Graphics, IEEE Transactions on  
Volume 11, Issue 5, Sept.-Oct. 2005 Page(s):562 - 572  
Digital Object Identifier 10.1109/TVCG.2005.79  
[AbstractPlus](#) | Full Text: [PDF](#)(1608 KB) IEEE JNL
- ☐ **14. Prefix and interval-partitioned dynamic IP router-tables**  
Haibin Lu; Kim, K.S.; Sahni, S.;  
Computers, IEEE Transactions on

Volume 54, Issue 5, May 2005 Page(s):545 - 557  
Digital Object Identifier 10.1109/TC.2005.83

[AbstractPlus](#) | Full Text: [PDF](#)(1536 KB) IEEE JNL



**15. Software Trace Cache**

Ramirez, A.; Larriba-Pey, J.L.; Valero, M.;  
Computers, IEEE Transactions on  
Volume 54, Issue 1, Jan 2005 Page(s):22 - 35  
Digital Object Identifier 10.1109/TC.2005.13

[AbstractPlus](#) | Full Text: [PDF](#)(1032 KB) IEEE JNL



**16. Pointer Analysis for Source-to-Source Transformations**

Buss, M.; Edwards, S.A.; Bin Yao; Waddington, D.;  
Source Code Analysis and Manipulation, 2005. Fifth IEEE International Worksh  
30-01 Sept. 2005 Page(s):139 - 150  
Digital Object Identifier 10.1109/SCAM.2005.20

[AbstractPlus](#) | Full Text: [PDF](#)(424 KB) IEEE CNF



**17. Visualization/animation of programs in Alma: obtaining different results**

Pereira, M.J.V.; Henriques, P.R.;  
Human Centric Computing Languages and Environments, 2003. Proceedings.  
Symposium on  
28-31 Oct. 2003 Page(s):260 - 262  
Digital Object Identifier 10.1109/HCC.2003.1260242

[AbstractPlus](#) | Full Text: [PDF](#)(287 KB) IEEE CNF



**18. XOGastan: XML-oriented gcc AST analysis and transformations**

Antoniol, G.; Di Penta, M.; Masone, G.; Villano, U.;  
Source Code Analysis and Manipulation, 2003. Proceedings. Third IEEE Intern  
Workshop on  
26-27 Sept. 2003 Page(s):173 - 182

[AbstractPlus](#) | Full Text: [PDF](#)(409 KB) IEEE CNF



**19. The utility of graph theoretic software metrics: a case study**

Demko, A.B.; Pizzi, N.J.;  
Electrical and Computer Engineering, 2003. IEEE CCECE 2003. Canadian Co  
Volume 2, 4-7 May 2003 Page(s):1309 - 1312 vol.2

[AbstractPlus](#) | Full Text: [PDF](#)(329 KB) IEEE CNF



**20. Research on VHDL RTL synthesis system**

Haifeng Zhou; Zhenghui Lin; Wei Cao;  
Electronic Design, Test and Applications, 2002. Proceedings. The First IEEE I  
Workshop on  
29-31 Jan. 2002 Page(s):99 - 103  
Digital Object Identifier 10.1109/DELTA.2002.994596

[AbstractPlus](#) | Full Text: [PDF](#)(231 KB) IEEE CNF



**21. Syntax-directed construction of Value Dependence Graphs**

Byers, D.; Kamkar, M.; Palsson, T.;  
Software Maintenance, 2001. Proceedings. IEEE International Conference on  
7-9 Nov. 2001 Page(s):692 - 703  
Digital Object Identifier 10.1109/ICSM.2001.972788

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